

Phase Noise and Jitter Measurements



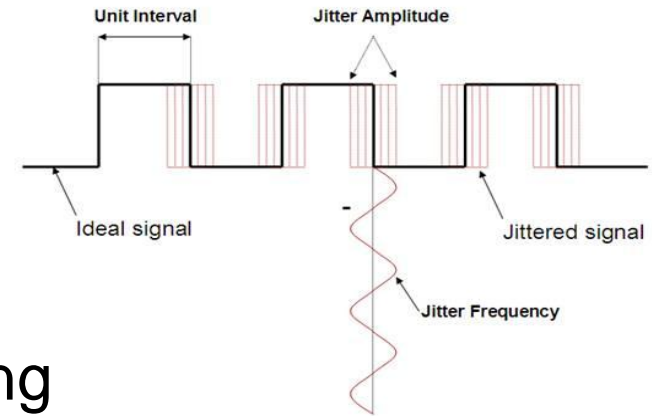
ROHDE & SCHWARZ

Agenda

- **Jitter Review**
- **Time-Domain and Frequency-Domain Jitter Measurements**
- **Phase Noise Concept and Measurement Techniques**
- **Deriving Random and Deterministic Jitter from Phase Noise**
- **PLL/Filter Weighting of Jitter Spectrum**
- **Calculating Peak-to-Peak Jitter from RMS Jitter**



What is Jitter?



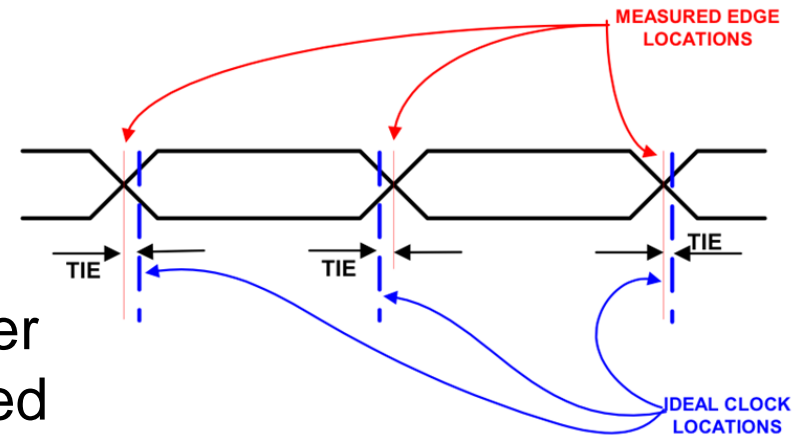
- Jitter is the short-term time-domain variations in clock or data signal timing
- Jitter includes instability in signal period, frequency, phase, duty cycle or some other timing characteristic
- Jitter is of interest from cycle to cycle, over many consecutive cycle, or as a longer term variation
- Jitter is equivalent to Phase Noise in the frequency domain
- Variations with frequency components $>10\text{Hz}$ are *Jitter*
- Variations with frequency components $<10\text{Hz}$ are *Wander*



Types of Jitter

- **Time Interval Error (TIE)**

- Fundamental measurement of jitter
- Time difference between measured signal edge and ideal edge
- Instantaneous phase of signal



- **Period Jitter**

- Short-term stability, basic parameter for clocks

- **Cycle to Cycle**

- Important for parallel data transfer

- **N-Cycle**

- Important when clock and data routing differ



Jitter Measurement Techniques

- **Time Domain (Oscilloscope)**

- Direct method for measuring jitter
- Measures TIE, Period Jitter, Cycle-to-Cycle Jitter
- Measures RMS or Peak-to-Peak Jitter
- Measures data or clock signals
- Limited sensitivity (100 – 1000 fs)



- **Frequency Domain (Phase Noise Analyzer)**

- Calculates jitter from phase noise
- Measures RMS Jitter
- Measures clocks, not random data streams
- Easy to separate random and discrete jitter components ✓
- Highest sensitivity (<5 fs) ✓



What is Phase Noise?

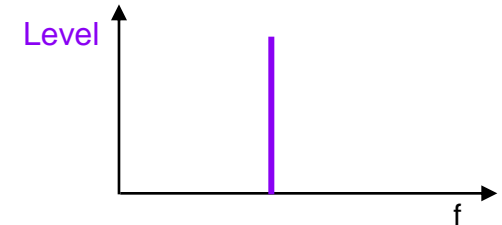
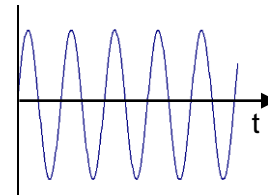
- **Ideal Signal (noiseless)**

$$V(t) = A \sin(2\pi\nu t)$$

where

A = nominal amplitude

ν = nominal frequency



Time Domain
↑
↓

Frequency Domain
↑
↓

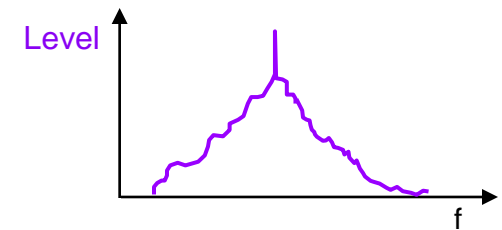
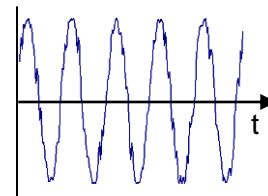
- **Real Signal**

$$V(t) = [A + E(t)] \sin(2\pi\nu t + \phi(t))$$

where

E(t) = amplitude fluctuations

$\phi(t)$ = phase fluctuations

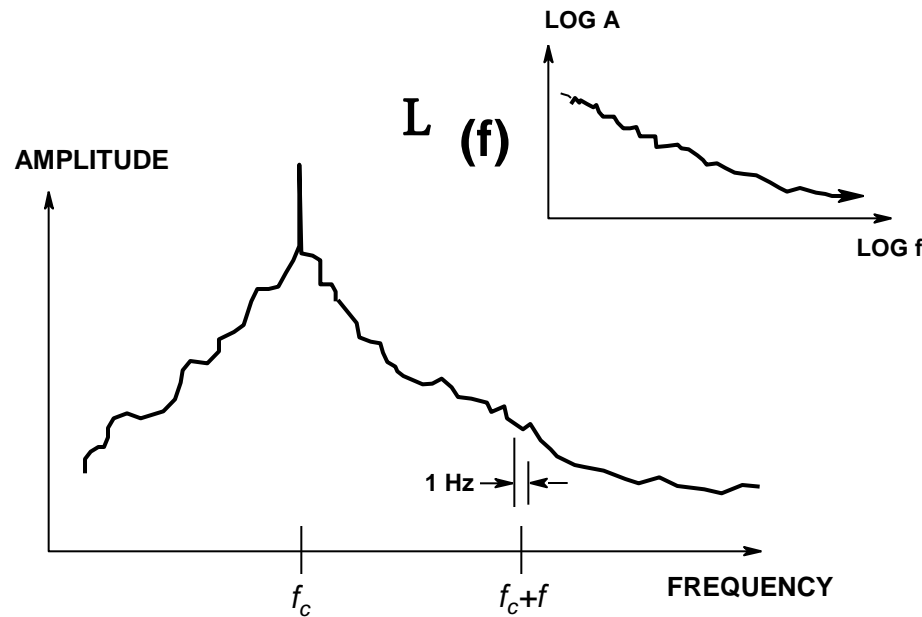


Phase Noise is unintentional phase modulation that spreads the signal spectrum in the frequency domain.

Phase Noise is equivalent to jitter in the time domain.

Phase Noise – Unit of Measure

- Phase Noise is expressed as $L(f)$
- $L(f)$ is defined as single sideband power due to phase fluctuations in a rectangular 1Hz bandwidth at a specified offset, f , from the carrier
- $L(f)$ has units of dBc/Hz

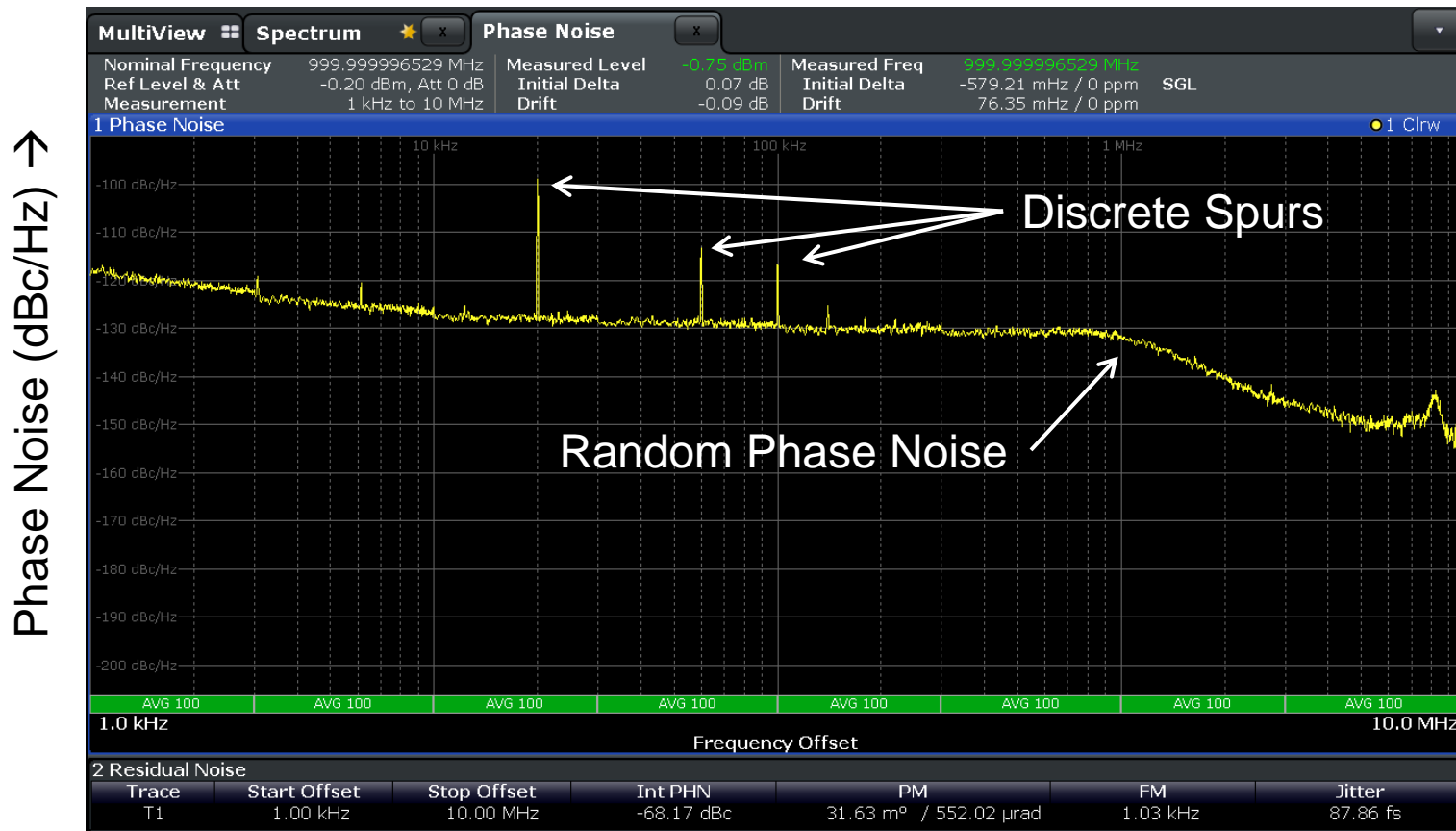


Phase Noise Measurement Setup

**Clock
Under
Test**



Example Phase Noise Measurement Plot



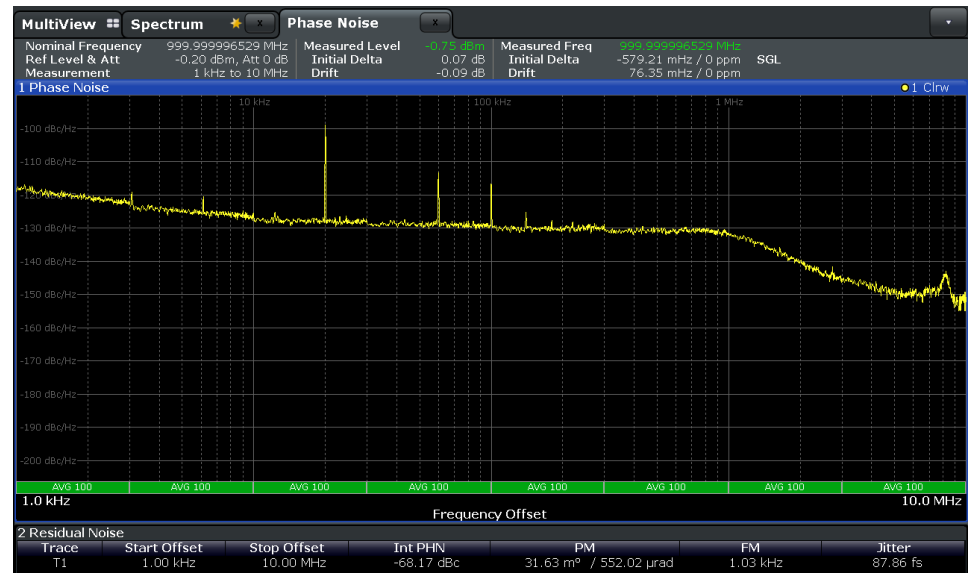
Offset from Fundamental Frequency →

Phase Noise Measurement

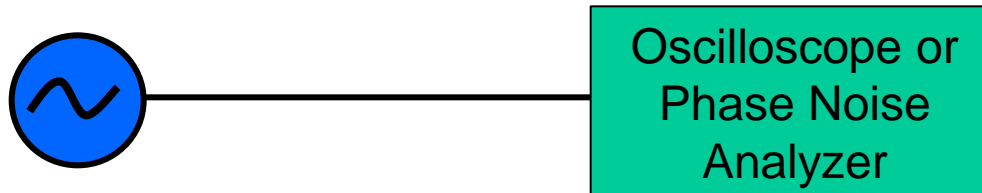
- Shows phase noise over a range of offset frequencies: $L(f)$

- $$\text{RMS Jitter} = \frac{1}{2\pi f_c} \sqrt{2 \int L(f) df}$$

- Phase noise including spurs yields TJ, or Total Jitter (random plus deterministic)
- Phase noise without spurs yields RJ, or Random Jitter



Jitter/Phase Noise Measurements: Golden Rule

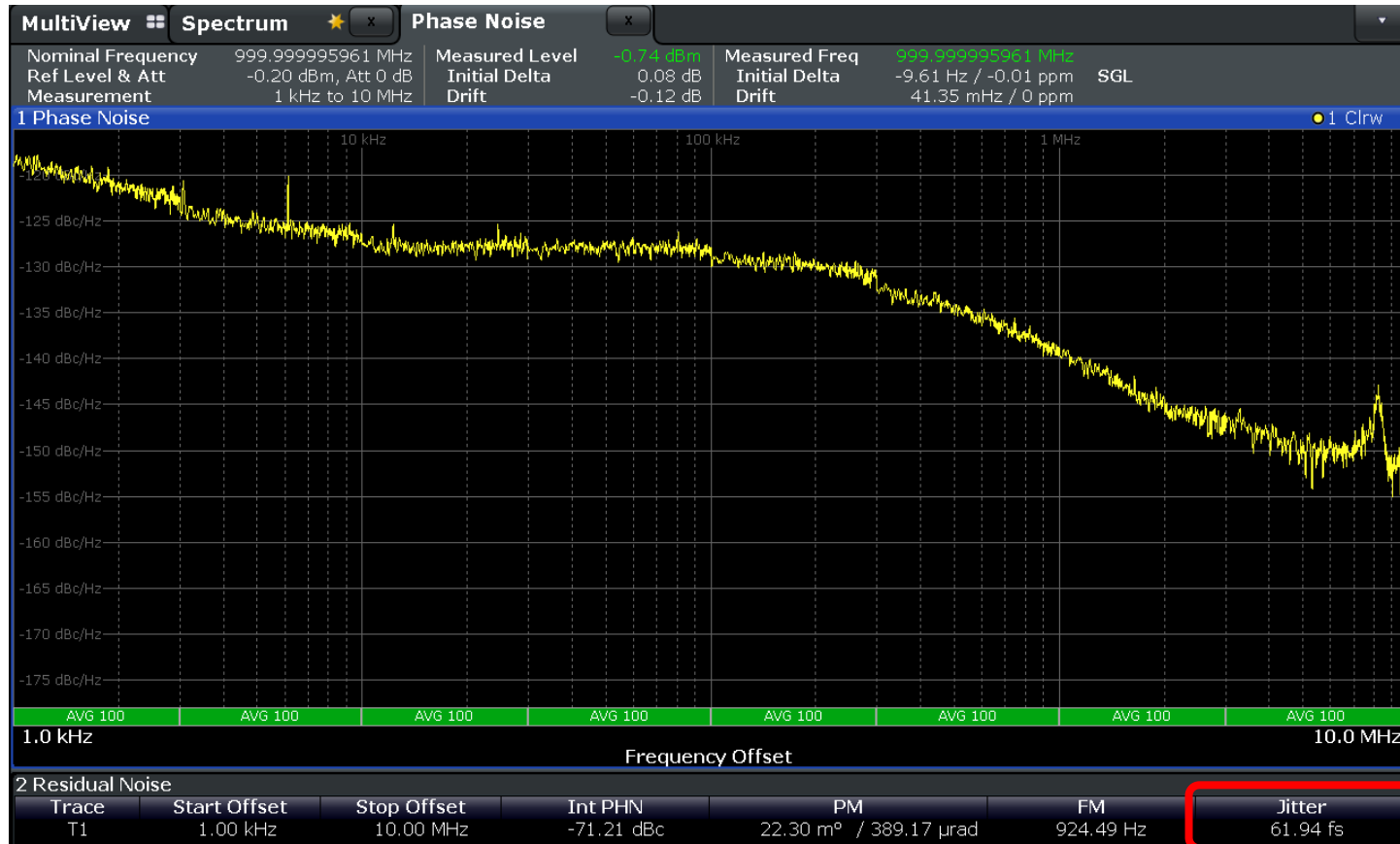


- Jitter measured by an oscilloscope or phase noise analyzer is always the RMS sum of the clock jitter and the internal jitter of the measuring instrument
- Internal jitter/phase noise limits measurement sensitivity
- Examples:
 - Clock Jitter: 1ps Instrument Jitter: 1ps → Measured Jitter: 1.4ps
 - Clock Jitter: 500fs Instrument Jitter: 1ps → Measured Jitter: 1.118ps
 - Clock Jitter: 500fs Instrument Jitter: 300fs → Measured Jitter: 583fs
 - Clock Jitter: 200fs Instrument Jitter: 5fs → Measured Jitter: 200.06fs



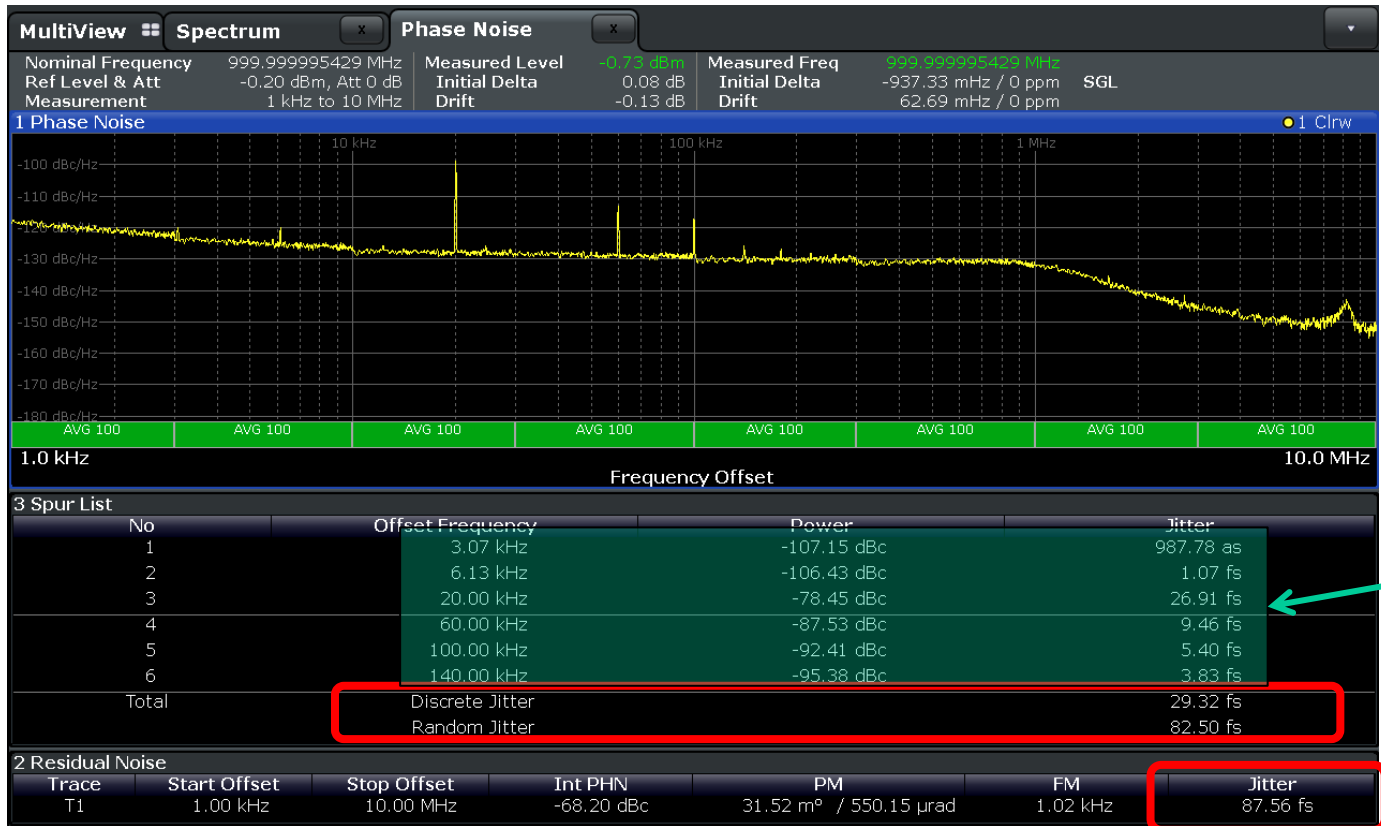
Measurement on FSW Spectrum Analyzer

- Total RMS Jitter (RJ): 61.94 fs



Measurement on FSW Spectrum Analyzer (w/spurs)

- DJ: 29.32 fs RJ: 82.50 fs TJ: 87.56 fs



Individual discrete jitter contributions

Phase Noise Measurement Instruments

- Spectrum analyzer (with a phase noise personality option) is a good way to measure phase noise/jitter
- Sensitivity is limited by spectrum analyzer architecture and internal local oscillator phase noise
- Phase noise analyzer (or Signal Source Analyzer) uses a different measurement technique to get the best possible sensitivity



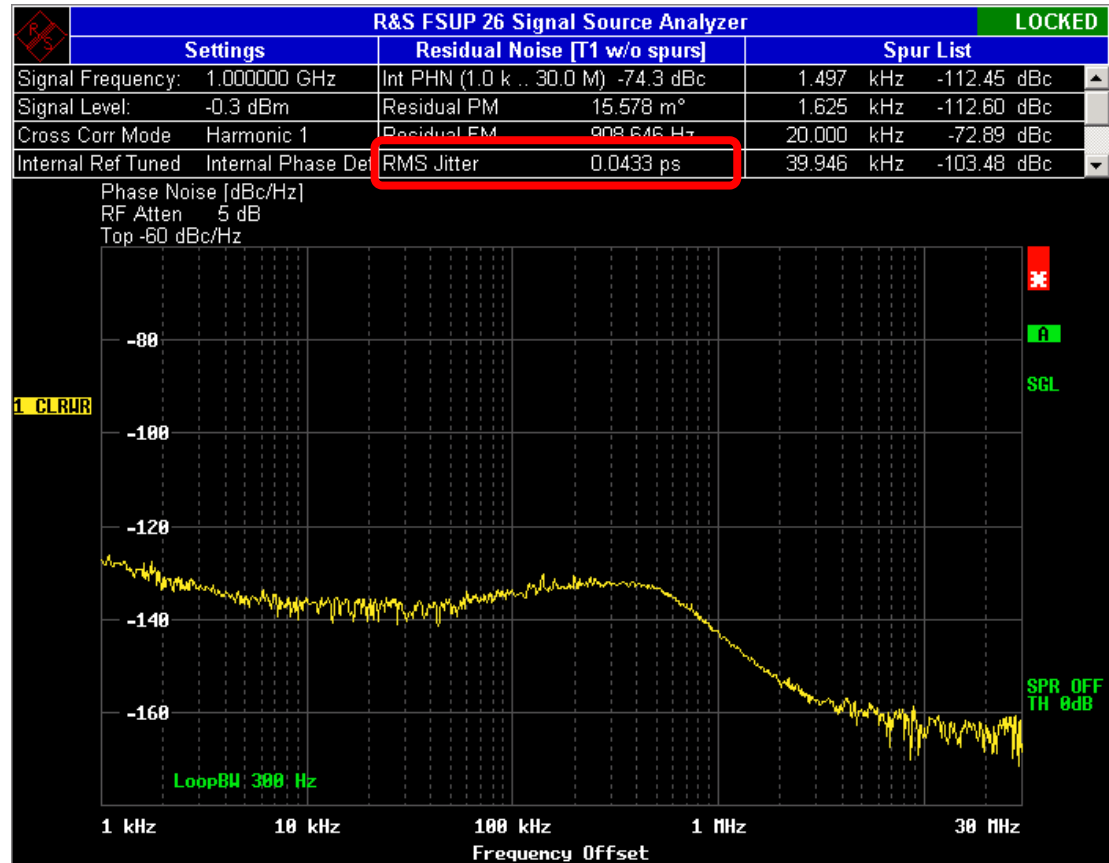
FSW Spectrum Analyzer



FSUP Signal Source Analyzer

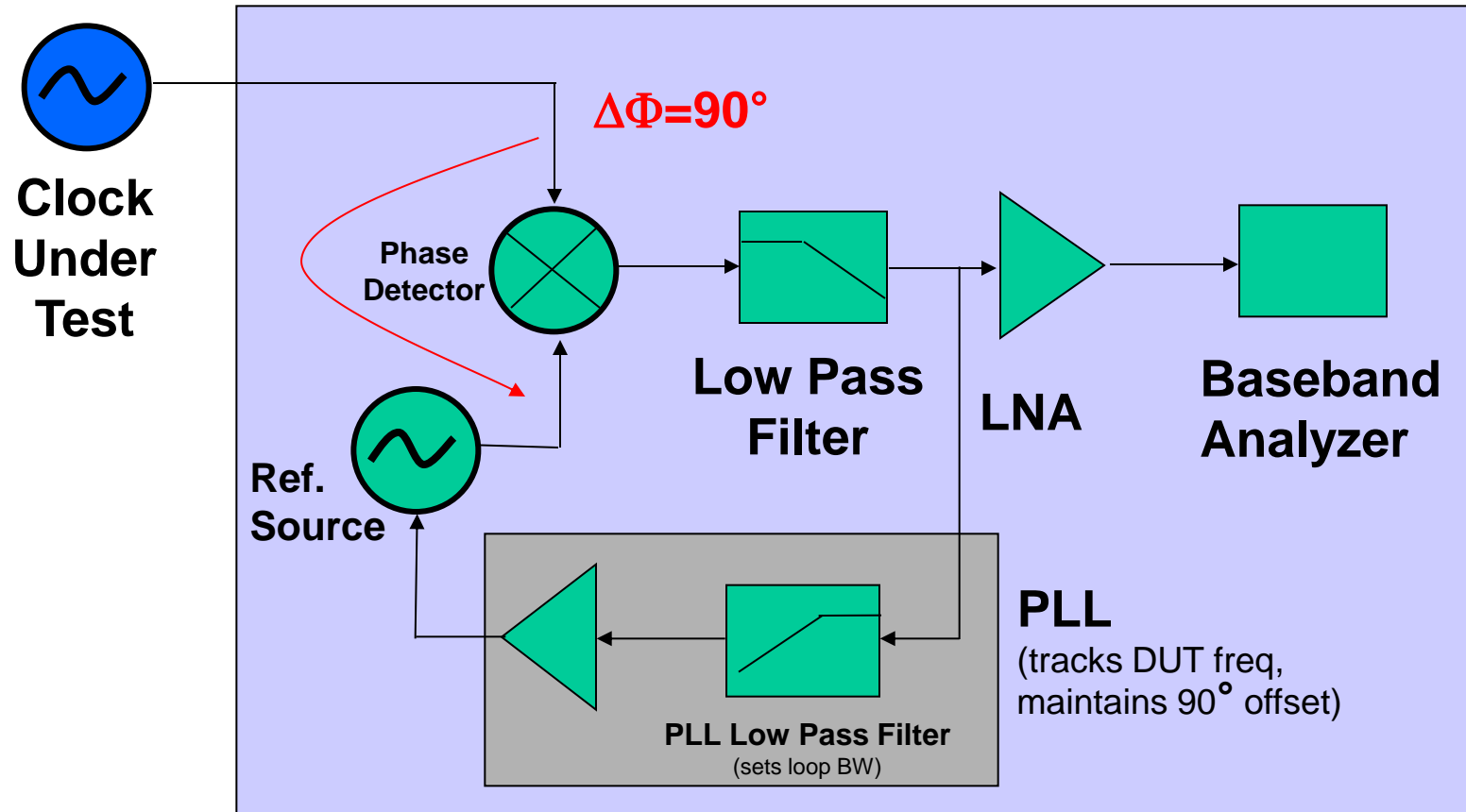
Phase Noise Measurement on FSUP

- Total RMS Jitter (RJ)
 - 43.3 fs



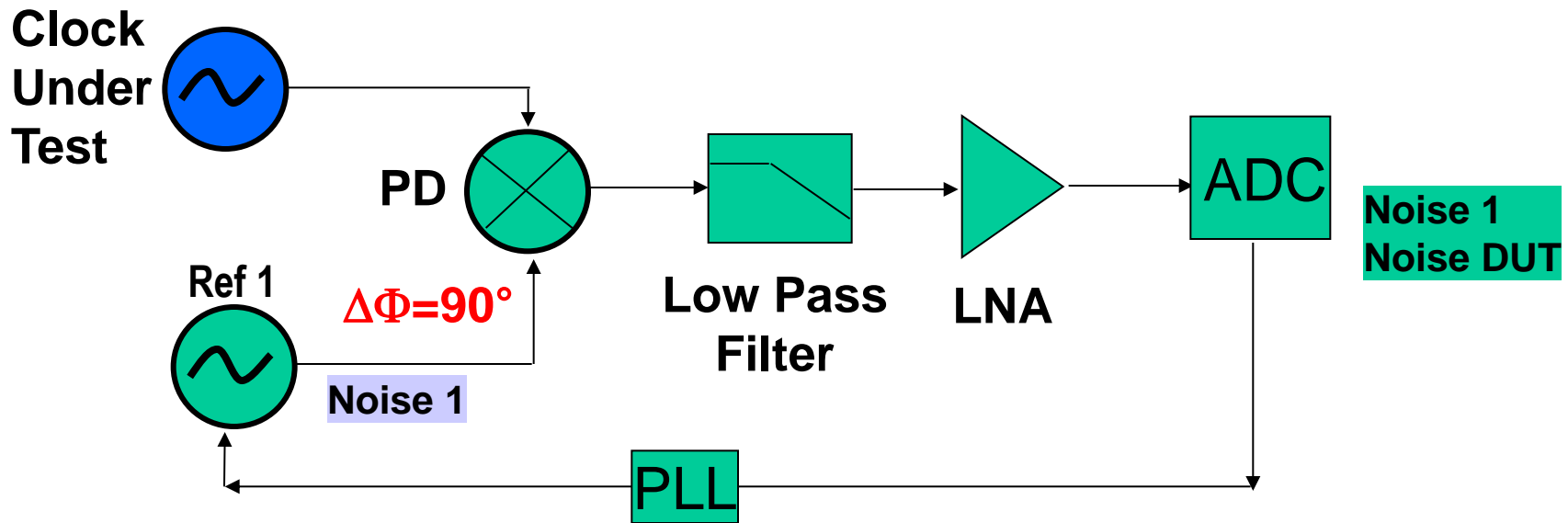
Phase Noise Measurement

Phase Detector Technique

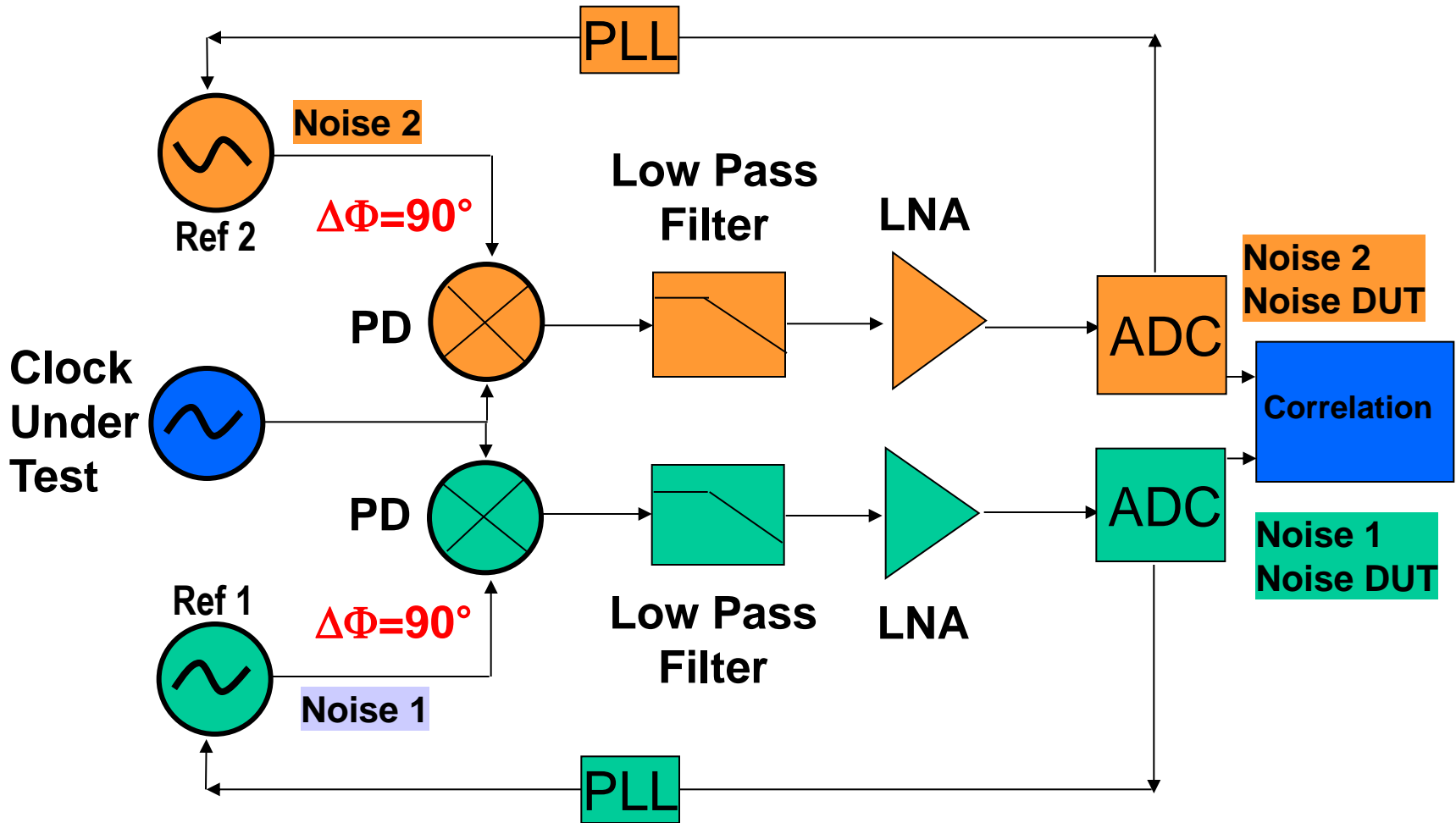


Reference source is tuned to same frequency as clock with 90° phase offset (quadrature)

Phase Detector without Cross-Correlation



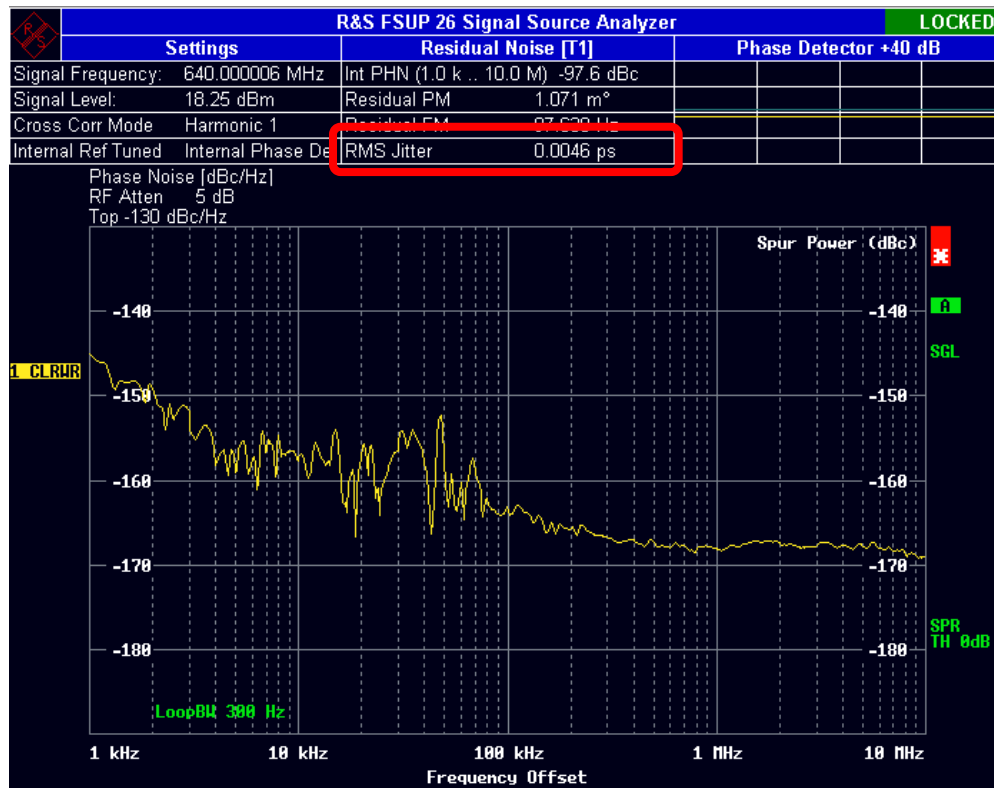
Phase Detector with Cross-Correlation



Cross-correlating both measurements reduces effective noise from reference sources up to 20dB – up to 10x improvement in jitter measurement sensitivity

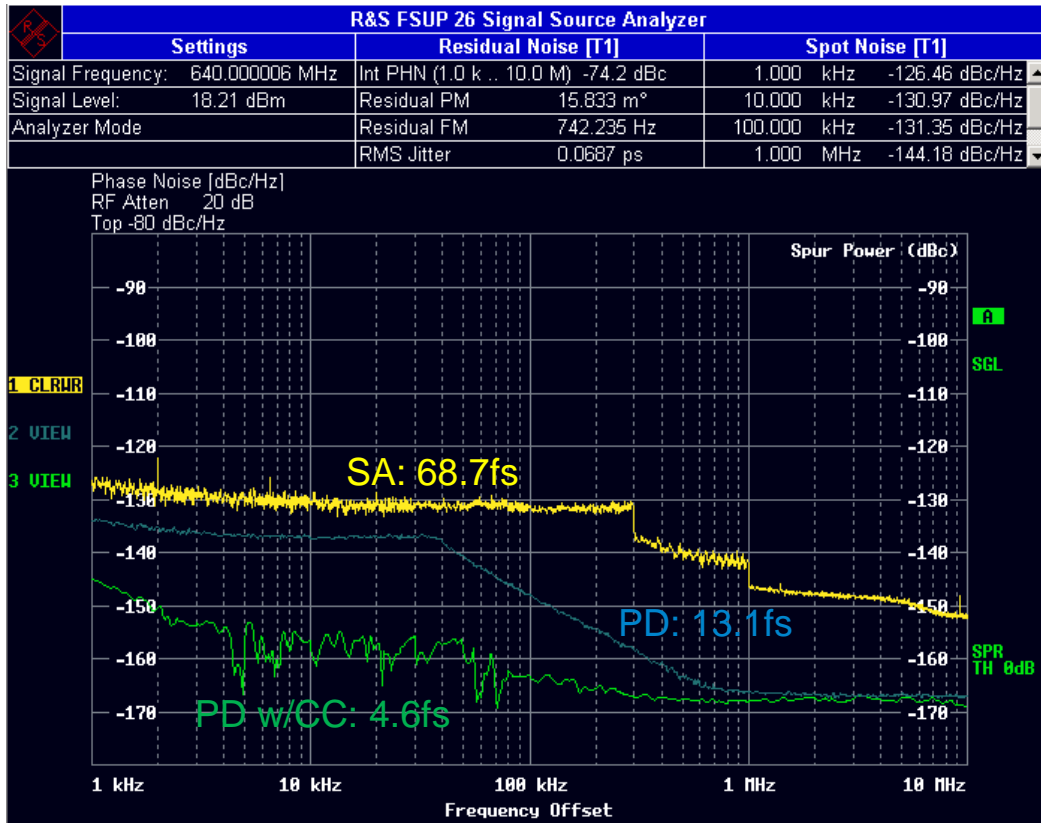
Measurement of a Very Low Jitter Device with FSUP

- Crystal based 640MHz oscillator with very low phase noise/jitter
 - **4.6fs**
- Cross-correlation technique provides this measurement sensitivity



Phase Noise/Jitter Measurement

Spectrum Analyzer vs Phase Detector vs PD with Cross-Correlation



Same signal measured with three different techniques

Phase Detector with Cross-Correlation is the most sensitive way to measure phase noise and jitter

Jitter Measurement Instruments

Real time (Oscilloscope)

- Single-shot or repetitive events (clock or data)
- Bandwidths typically 60 MHz to >30 GHz
- Lowest sensitivity (highest jitter noise floor)
- Measures adjacent cycles

Repetitive (Sampling Oscilloscope)

- Repetitive events only (clock or data)
- Bandwidths typically 20 GHz to 100 GHz
- Generally can not discriminate based on jitter frequency
- Cannot measure adjacent cycles

Phase noise (SA / Phase Noise Analyzer)

- Clock signals only (50% duty cycle)
- Integrate phase noise over frequency to measure jitter
- Highest sensitivity (lowest jitter noise floor)
- Cannot measure adjacent cycles



High
Sensitivity

High
Flexibility



Phase Noise Measurement (including spurs)

- FSUP Phase Noise Analyzer requires manual calculation of discrete jitter

- Total RMS Jitter (RJ & DJ)
 - 67.5 fs

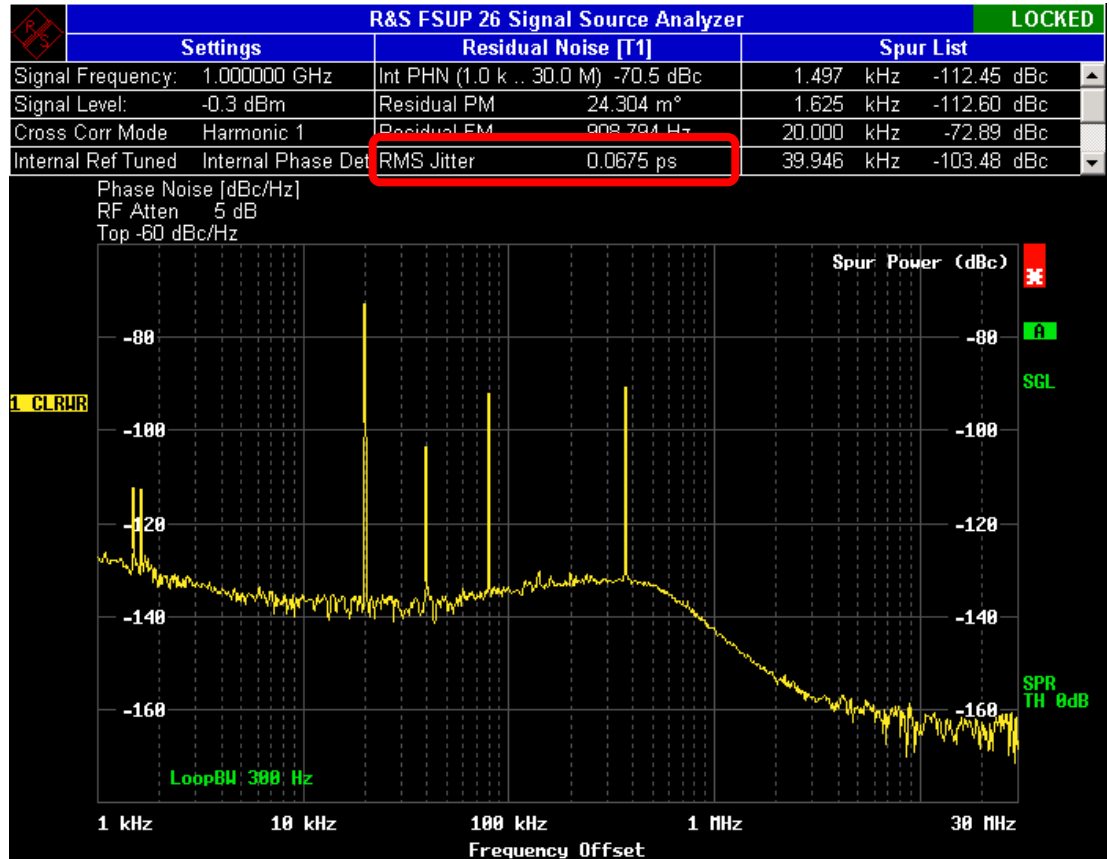
- Total Jitter (TJ) is RMS sum of RJ and DJ:

$$TJ = \sqrt{RJ^2 + DJ^2}$$

- DJ can be calculated as:

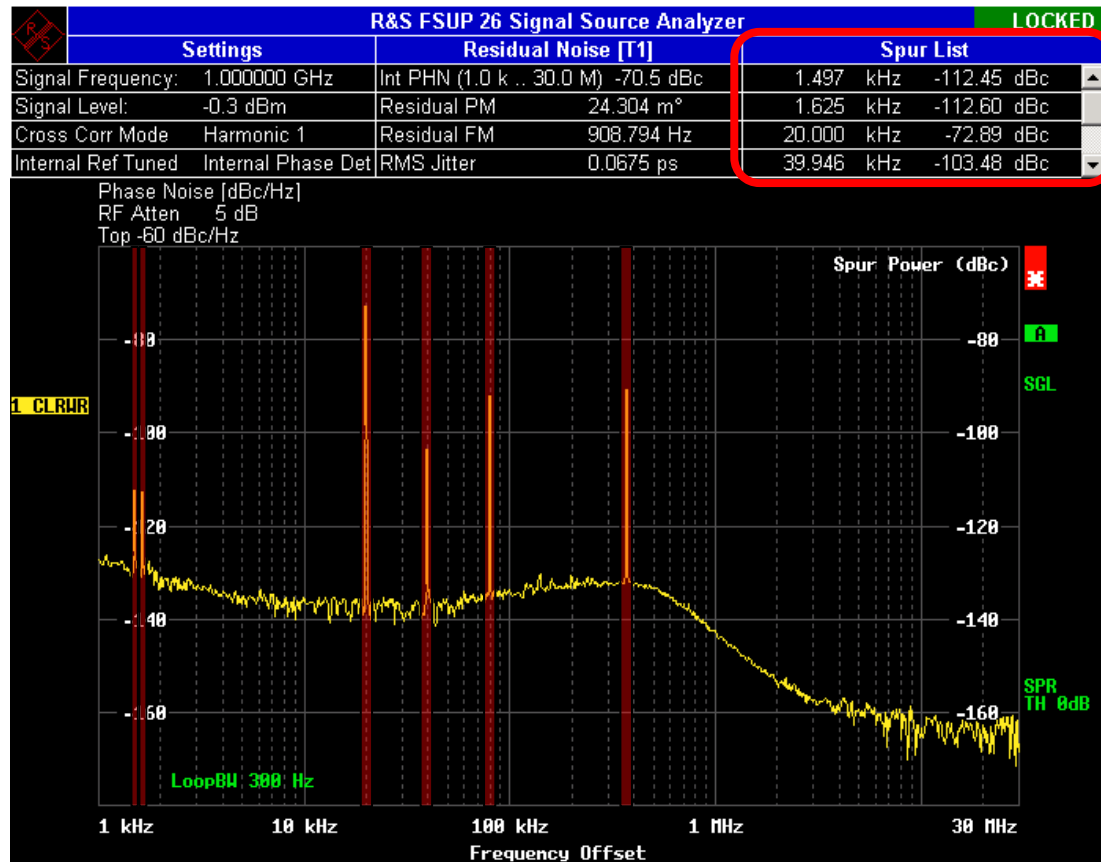
$$DJ = \sqrt{TJ^2 - RJ^2}$$

- TJ = 67.5 fs, RJ = 43.3 fs
 - Calculated DJ = 51.8 fs



Measurement of DJ from Individual Contributors

- What is the contribution of individual discrete components (spurs) to total RMS jitter?
- Use the spur level values from the Spur List



Measurement of DJ from Individual Contributors

- General formula to convert phase noise to jitter is: $\frac{1}{2\pi \cdot f_c} \sqrt{2 \int L(f) df}$
- Integral under the square root, $\int L(f) df$, is “integrated phase noise”
- For discrete spurs the integrated phase noise is simply the ‘dBc’ level
- Jitter for a spur can be calculated from its dBc level using: $\frac{10^{dBc/20}}{\sqrt{2} \cdot \pi \cdot f_c}$
- Example: 20kHz spur at -72.889dBc on a 1GHz clock:

Spur List		
1.497	kHz	-112.45 dBc
1.625	kHz	-112.60 dBc
20.000	kHz	-72.89 dBc
39.946	kHz	-103.48 dBc

→

spurlist.txt - Notep...

File Edit Format View Help

```
Spurs; 6;
1497.016479; -112.445526;
1625.111694; -112.600953;
19999.998047; -72.889084;
39946.191406; -103.483963;
80000.570313; -91.960197;
370639.156250; -90.768600;
```

→

$$\frac{10^{dBc/20}}{\sqrt{2} \cdot \pi \cdot f_c} = \frac{10^{(-72.889/20)}}{\sqrt{2} \cdot \pi \cdot 10^9} = 51.04 fs$$

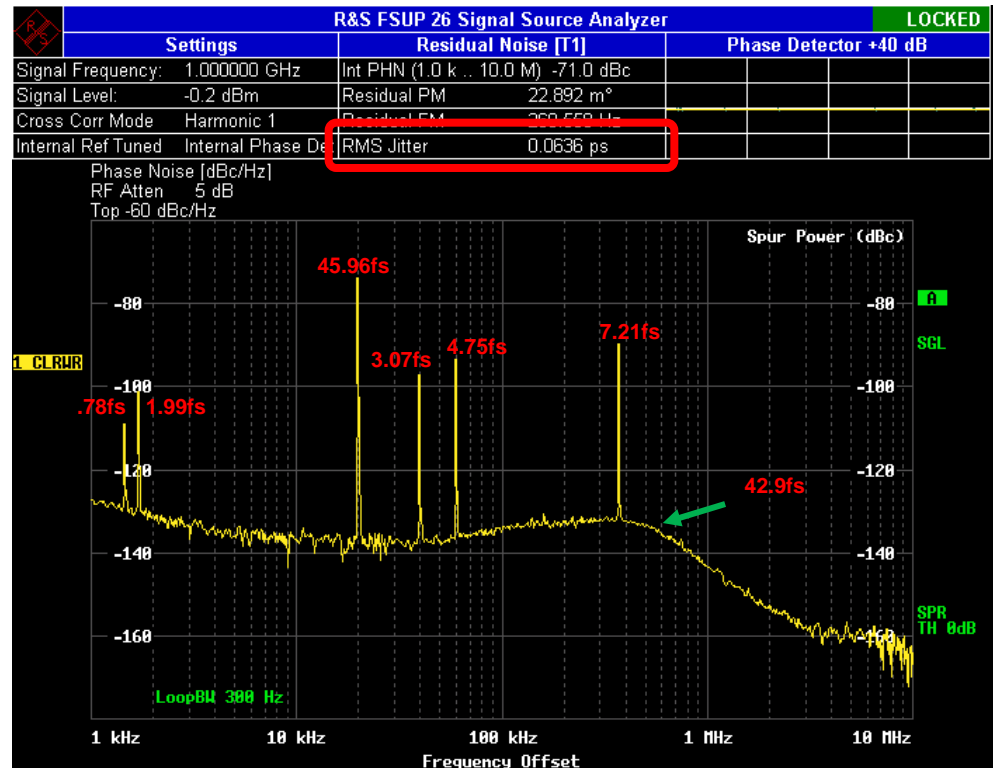
(exported spur list)

Summary of Total Jitter

- TJ is RSS of all contributors

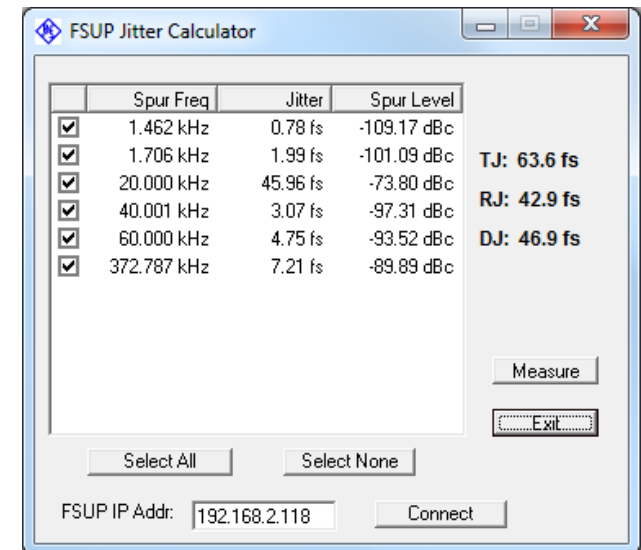
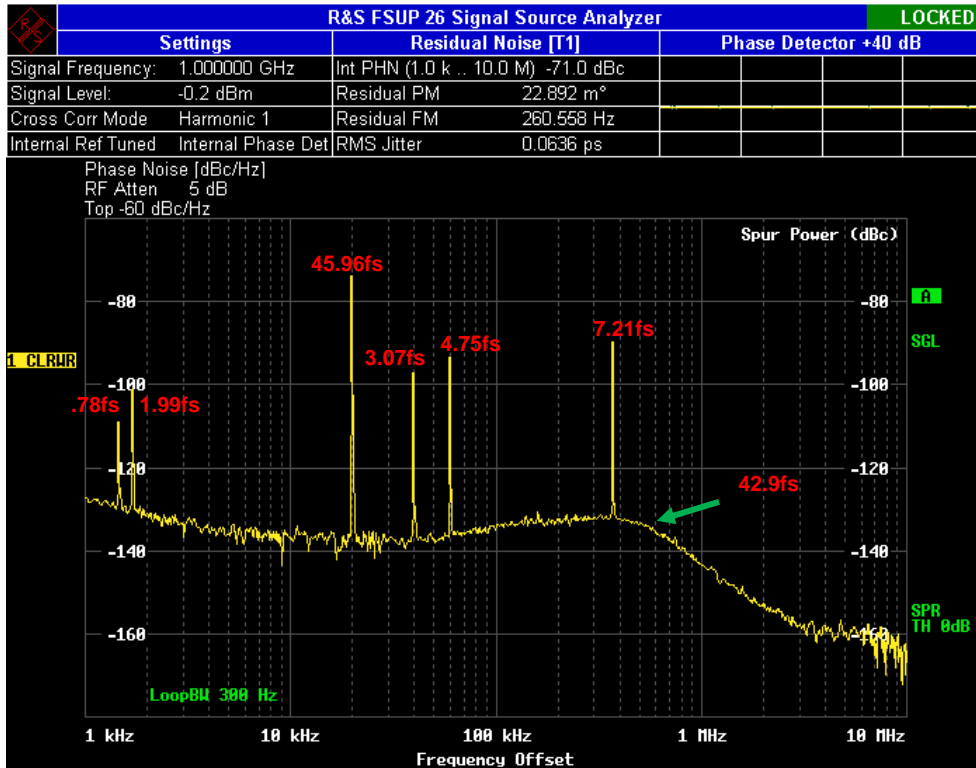
$$TJ = \sqrt{DJ_1^2 + DJ_2^2 + DJ_3^2 + DJ_4^2 + DJ_5^2 + DJ_6^2 + RJ^2}$$

$$= \sqrt{.78^2 + 1.99^2 + 45.96^2 + 3.07^2 + 4.75^2 + 7.21^2 + 42.9^2} = 63.6 \text{ fs}$$



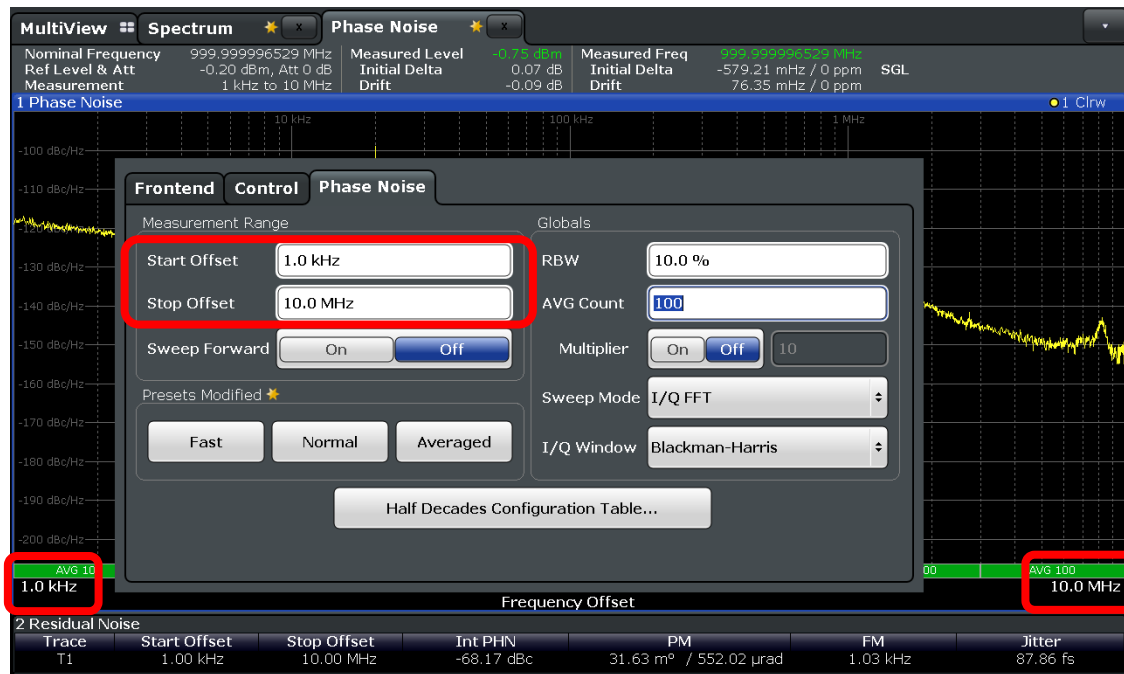
Summary of Total Jitter

- A simple utility can automate these calculations



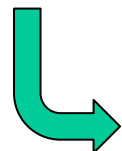
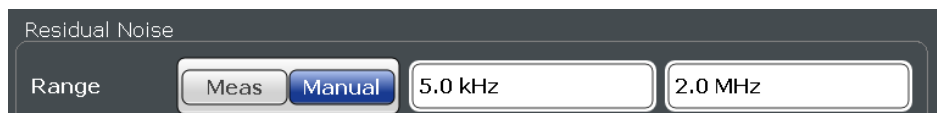
Frequency Offset Range is Settable

- Measurements in this presentation have used offset range of 1kHz to 10MHz or 30MHz
- Upper offset range can be as high as 30GHz
- Lower offset can be as low as 1Hz on a SA or 10mHz on a Phase Noise Analyzer

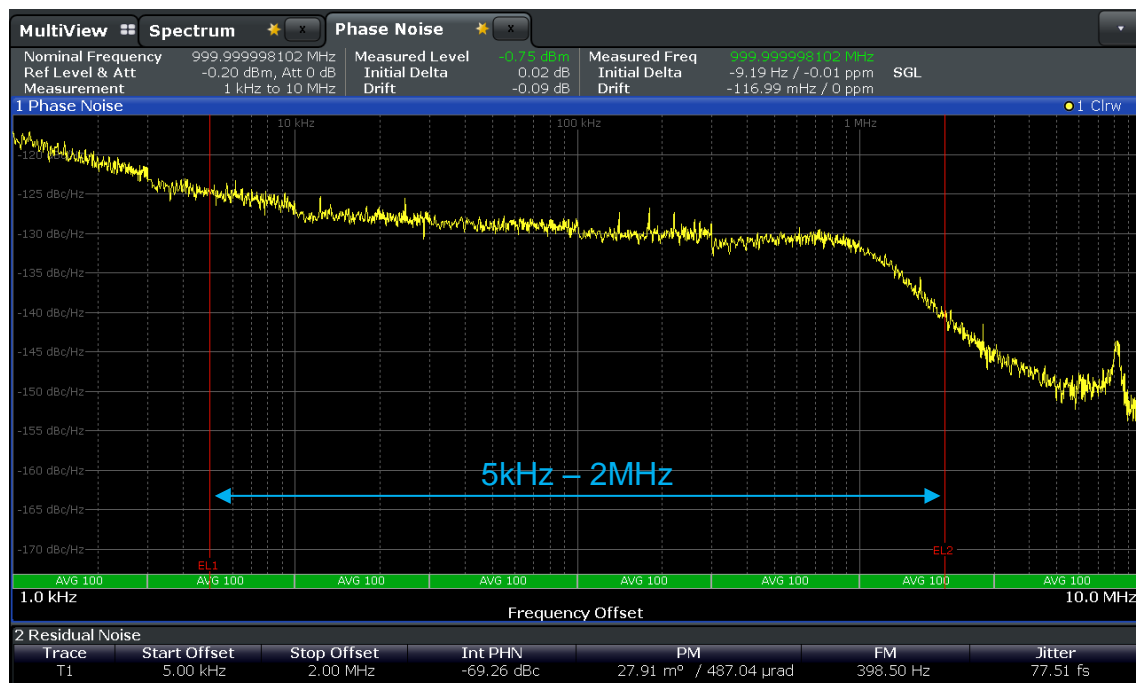


Jitter Calculation over Subset of Measured Range

- By default, jitter is calculated over entire measured offset range
- A subset of the offset range may be specified for the jitter calculation



- Jitter calculated over full measured range of 1kHz – 10MHz is **83.09fs**
- For reduced range of 5kHz – 2MHz it is **77.51fs**



Jitter Calculation with PLL Weighting

- Basic measurement shows raw performance of clock
- Real systems use PLLs
- FSUP can apply a weighting function to simulate the frequency response of a PLL

Define PLL Freq Response

Name	PLL1
Comment	
Frequency	Factor Value/dB
1 kHz	40 dB
10 kHz	20 dB
100 kHz	0 dB
3 MHz	0 dB
10 MHz	10 dB
...	...



Residual Calculations

Use Meas Settings

Eval From 1.00 kHz

To 10.00 MHz

Apply Transducer Factors PLL1

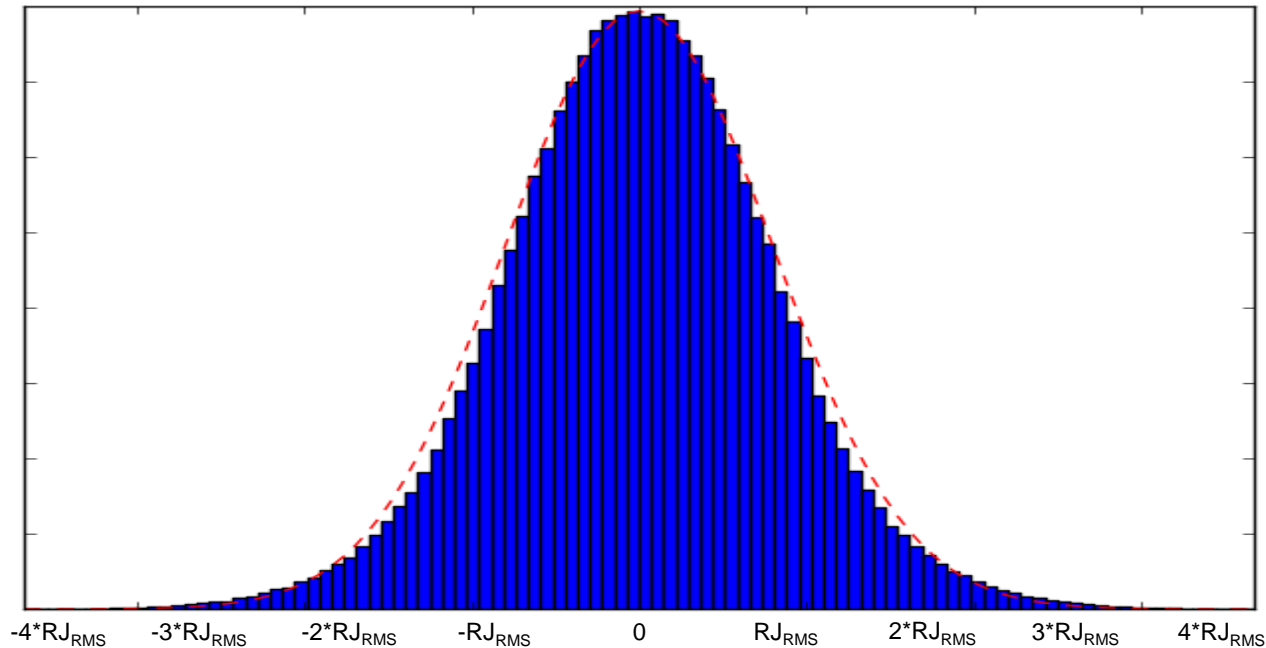


Select PLL to apply to measurement



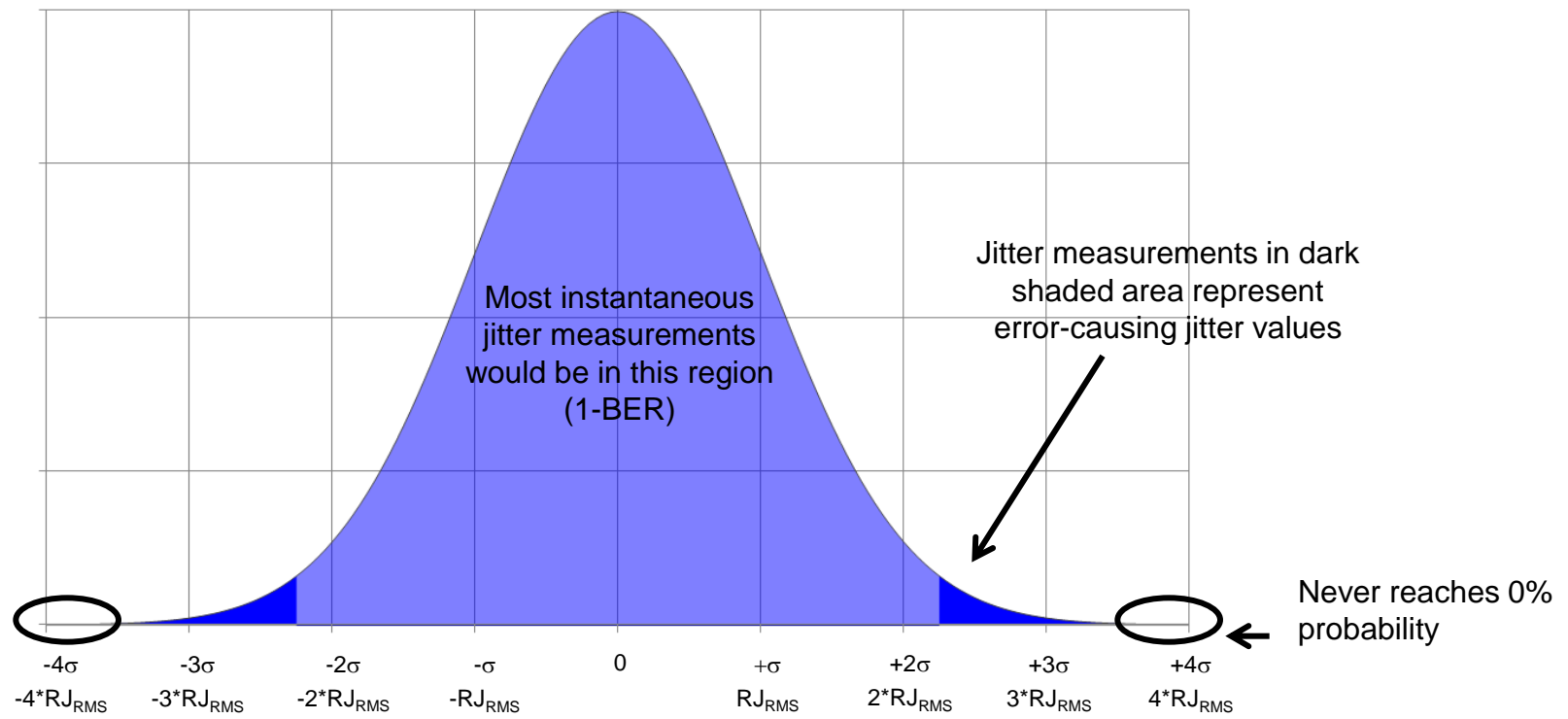
Calculating Peak-to-Peak Jitter from RMS Jitter

- Time-domain histogram of many oscilloscope-based jitter measurements shows a Gaussian distribution when jitter is purely random (RJ)
- The standard deviation (σ) is the RMS jitter (RJ)



Calculating Peak-to-Peak Jitter from RMS Jitter

- Phase noise measurement doesn't provide a histogram, but does provide RMS jitter value (and therefore standard deviation)
- RJ has a Gaussian distribution so we can calculate pk-pk jitter for a given BER
 - Example: if $BER=10^{-6}$ then we want 999,999 of 1,000,000 jitter measurements to fall in light shaded region, only 1 in dark shaded region



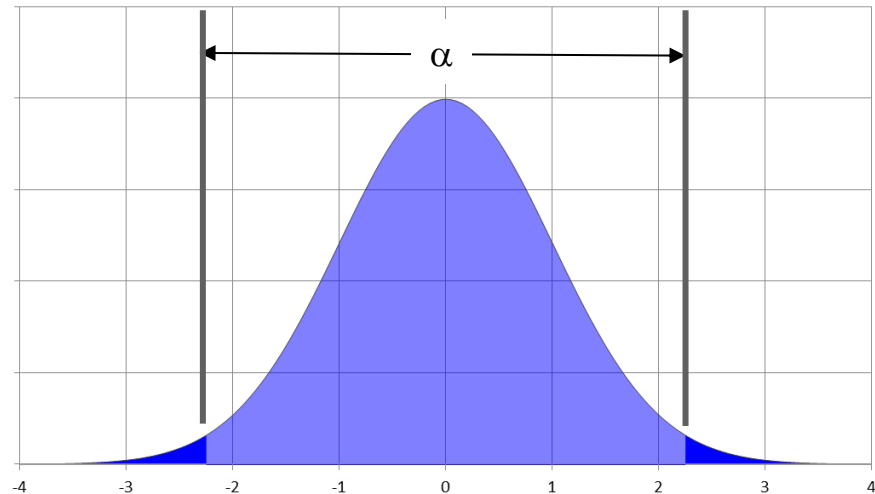
Calculating Peak-to-Peak Jitter from RMS Jitter

- We can use the complimentary Gaussian Error Function, $\text{erfc}(x)$, to calculate peak-to-peak random jitter from RMS jitter

$$RJ_{pp} = \alpha * RJ_{RMS} \quad \text{where } \alpha \text{ is derived from: } \frac{1}{2} \text{erfc}\left(\frac{\alpha}{2\sqrt{2}}\right) = BER$$

- Not closed form so use lookup table

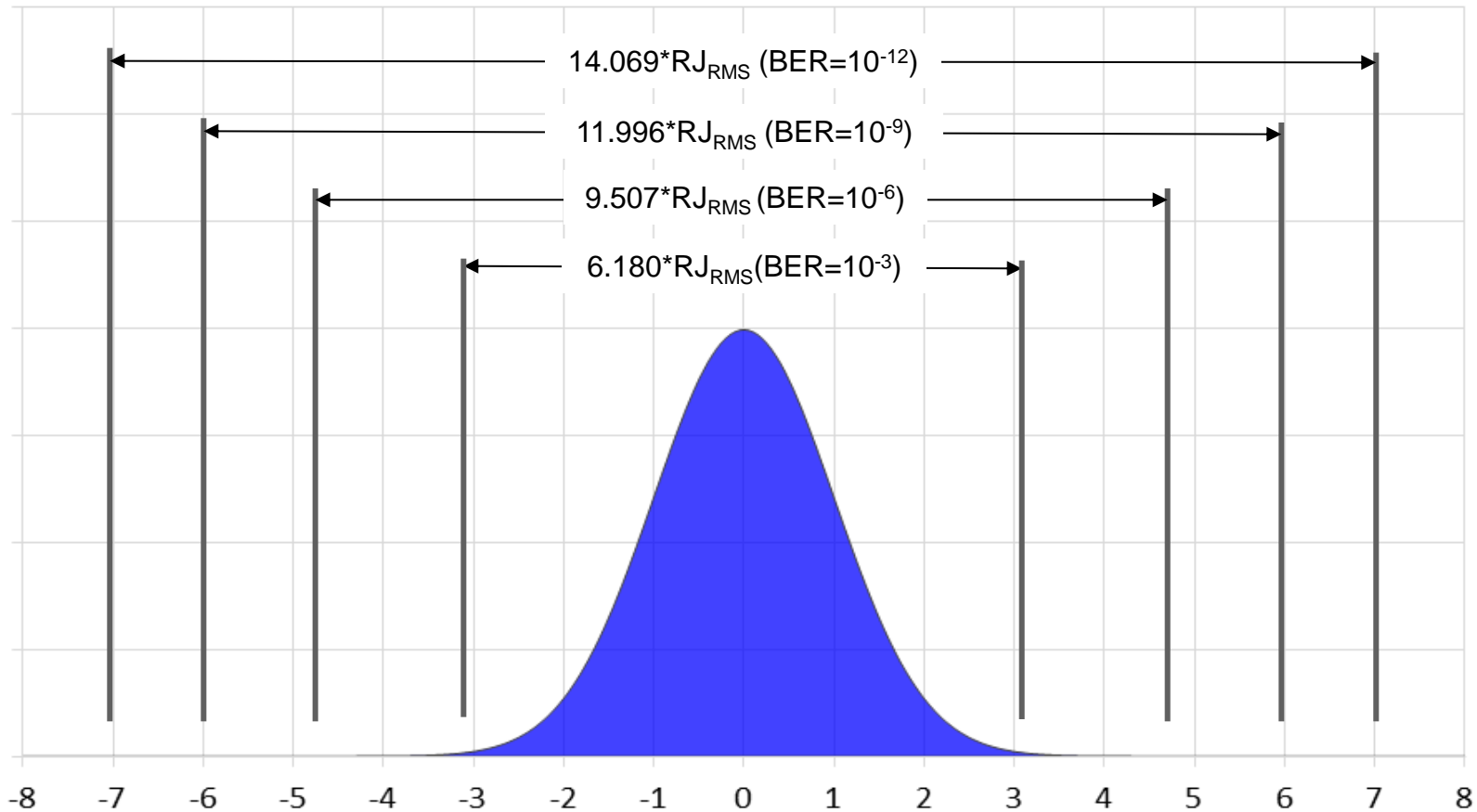
BER	α
10^{-3}	6.180
10^{-4}	7.438
10^{-5}	8.530
10^{-6}	9.507
10^{-7}	10.399
10^{-8}	11.224
10^{-9}	11.996
10^{-10}	12.723
10^{-11}	13.412
10^{-12}	14.069
10^{-13}	14.698
10^{-14}	15.301
10^{-15}	15.883
10^{-16}	16.444



Source: Maxim Application Note AN462

α vs. BER

- Factors to calculate RJ_{pp} from RJ_{RMS} based on BER
 - Example: $RJ_{pp} = 9.507 * RJ_{RMS}$ for $BER = 10^{-6}$



Useful References

- “Analysis of Jitter with the R&S FSUP Signal Source Analyzer”
Rohde & Schwarz Application Note 1EF71
- “Converting Between RMS and Peak-to-Peak Jitter at a Specified BER”
Maxim Integrated Application Note HFAN-4.0.2
- “Clock Jitter and Measurement”
SiTime Application Note SiT-AN10007
- “A Primer on Jitter, Jitter Measurement and Phase-Locked Loops”
Silicon Labs Application Note AN687
- “Determining Peak to Peak Frequency Jitter”
Pletronics White Paper



Thanks for your
attention!

